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2 A directed-graph classifier of semiconductor wafer-test patterns

Cresswell, M.W.; Khera, D.; Linholm, L.W.; Schuster, C.E.;

Semiconductor Manufacturing, IEEE Transactions on , Volume: 5 , Issue: 3 , , 1992

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3 Process challenges and solutions for embedding Chip-On-Board into mainstream SMT assembly

Luthra, M.;

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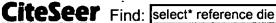
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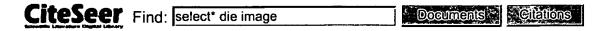
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